



41

28/2

XA-9631

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Hisao YAMAGATA et al.

Serial No.: 10/083,503 ✓

Group Art Unit: 2812

Filed: February 27, 2002

Examiner: O. Luk

For: SEMICONDUCTOR DEVICE MANUFACTURING METHOD AND
SEMICONDUCTOR DEVICE SORTING SYSTEM TO BE USED WITH
THE SAME

* * *

RESPONSE

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Responding to the Office Action mailed June 27, 2003,
and to the requirement for restriction therein, Applicants
elect Group I (Claims 1-27) for examination, reserving the
right to file a divisional application with regard to the
non-elected invention.

An action on the merits of the claims to the elected
invention is respectfully requested.

The Commissioner is hereby authorized to charge to
Deposit Account No. 50-1165 any fees under 37 C.F.R. §§1.16
and 1.17 which may be required by this paper, and to credit
any overpayment to that Account. If any extension of time

TECHNOLOGY CENTER 2800

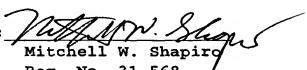
JUL 21 2003

RECEIVED

is required in connection with the filing of this paper and has not been requested separately, then such extension is hereby requested.

Respectfully submitted,

By:


Mitchell W. Shapiro
Reg. No. 31,568

MWS:lat

Miles & Stockbridge P.C.
1751 Pinnacle Drive
Suite 500
McLean, Virginia 22102
(703) 903-9000

July 16, 2003